

supplying the power control signal from the display driver means to the power source circuit, said power control signal having a delay time after the detection of said logic power voltage;

supplying the display driving voltages to the display driver means in response to the power control signal by the power source circuit; and

selecting the display driving voltages supplied from the power source circuit to the flat display panel by the display driver means.

2
28. The method according to Claim *27*, further comprising the step of supplying a start signal controlling a start of display to the display driver means after supplying the display driving voltages to the display driver means.

B1
3
29. A method of controlling a flat display device comprising a flat display panel module unit and a display control unit for supplying control signals to control display of the flat display panel module unit, said flat display panel module unit including a flat display panel driven in accordance with display driving voltages, display driver means for selecting the display driving voltages to the flat display panel and a display power source circuit for supplying the display driving voltages to the display driver means in response to a power control signal, the method of controlling the flat display unit comprising the steps of:

D
supplying the power control signal to the power source circuit by the display driver means, the power control signal having a delay time after a logic power voltage has ^{been} supplied to a logic circuit of the flat display device;

supplying the display driving voltages to the display driver means in response to the power control signal by the power source circuit;

supplying a display start signal controlling a start of the selection of the display driving voltages by the display driver means in response to the control signal supplied from the display control unit, said display start signal having a delay time after the power control signal has supplied to the power source circuit; and

selecting the display driving voltages supplied from the power source circuit to supply to the flat display panel in response to the display start signal.

⁴
~~30.~~ ³ The method according to Claim ~~29~~, wherein the flat display panel module unit is arranged separately from the display control unit.

⁵
~~31.~~ A flat display unit comprising:

a flat display panel for being driven in accordance with display driving voltages;

display driver means for selecting the display driving voltages supplied to said flat display panel, said display driver means comprising a logic circuit and a detection means for detecting a logic power voltage activating said logic circuit and for supplying a power control signal having a delay time after the detection of the logic power voltage; and

a display power source circuit for supplying the display driving voltages to said display driver means in response to the power control signal.

⁶
~~32.~~ A flat display device comprising a flat display panel module unit and a display control unit for supplying control signals to control display of the flat display panel module unit,

said flat display panel module unit comprising:

a flat display panel driven in accordance with display driving voltages;

display driver means for selecting the display driving voltages supplied to said flat display panel and for supplying a power control signal having a delay time after a logic power voltage has ^{been} supplied to a logic circuit of said display driver means; and

a display power source circuit for supplying the display driving voltages to said display driving means in response to the power control signal,